

PROCESSING ARCHITECTURE HAVING A MATRIX TRANSPOSE CAPABILITY

ABSTRACT OF THE DISCLOSURE

According to the invention, a matrix of elements is processed in a processor. A first subset of matrix elements is loaded from a first location and a second subset of matrix elements is loaded from a second location. A third subset of matrix elements is stored in a first destination and a fourth subset of matrix elements is stored in a second destination. The loading and storing steps result from the same instruction issue.

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